

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:) Confirmation No.: 3048
Takuya TSURUME et al.) Examiner: David A. Zarneke
Serial No. 10/591,700) Group Art Unit: 2891
Filed: September 5, 2006)
For: METHOD FOR MANUFACTURING)
INTEGRATED CIRCUIT)

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. § 1.56 and 37 C.F.R. §§ 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. § 1.98(a).

This Information Disclosure Statement is being submitted before the issuance of a first Office Action on the merits, therefore, no fee is required.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§ 1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,


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